

Anirban Ghose

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Address: 36C Lake Road, Kolkata 700029, West Bengal, India

Research Interests:

Scheduling, Runtime Systems, Deep Learning

Education:

PhD, Computer Science and Engineering (2016-Present)
Indian Institute of Technology, Kharagpur
Advisor: Dr. Soumyajit Dey

Masters by Research, Computer Science and Engineering (2013-2016)
Indian Institute of Technology, Kharagpur
Advisor(s): Dr. Soumyajit Dey and Dr. Pabitra Mitra

B.Tech in Computer Science and Engineering (2009-2013)
Heritage Institute of Technology, Kolkata, Affiliation: WBUT

Higher Secondary Examination (2007-2009)
South Point High School, Kolkata, Affiliation: WBCHE

Secondary Examination (2006-2007)
South Point High School, Kolkata, Affiliation: WBBSE

9.27/10

8.63/10

89.57 %

92.15 %

Publications:

- **Divergence Aware Automated Partitioning of OpenCL Workloads**, ISEC 2016.
[Anirban Ghose](#), [Soumyajit Dey](#), [Pabitra Mitra](#), [Mainak Chaudhuri](#)
- **A Framework for OpenCL Task Scheduling on Heterogeneous Multicores**, Parallel Processing Letters 2017
[Anirban Ghose](#), [Lokesh Dokara](#), [Soumyajit Dey](#), [Pabitra Mitra](#)
- **Orchestration of Perception Systems for Reliable Performance in Heterogeneous Platforms**, DATE 2021
[Anirban Ghose](#), [Srijeeta Maity](#), [Arijit Kar](#) and [Soumyajit Dey](#).
- **Thermal Aware Adaptive Platform Management for Heterogeneous Embedded Systems**, EMSOFT 2021
[Srijeeta Maity](#), [Anirban Ghose](#), [Soumyajit Dey](#) and [Swarnendu Biswas](#)
- **PySchedCL: Leveraging Concurrency in Heterogeneous Data-Parallel Systems**, Transactions on Computers 2021
[Anirban Ghose](#), [Siddharth Singh](#), [Vivek Kulaharia](#), [Lokesh Dokara](#), [Srijeeta Maity](#) and [Soumyajit Dey](#)

Awards:

- **Intel India Research Fellowship 2020**,
Research Proposal: Exploring Memory Optimizations for Deep Learning Workloads on Heterogeneous Platforms
- **Qualcomm Innovation Fellowship 2018**,
Project Proposal: Intelligent ADAS Task Management on Heterogeneous Automotive Architectures
Team: [Anirban Ghose](#), [Srijeeta Maity](#), [Soumyajit Dey](#), [Pabitra Mitra](#)

Experience:

Oct'21 - Jul'22	Graduate Technical Intern , XPU Soc and Arch Ops Dept, Intel India Technology, Pvt. Ltd.
May'14 - Jul'16	Junior Project Assistant for Research Project - "Static Analysis based Partitioning Techniques for GPGPU Programs" sponsored by ISIRD SRIC
Sep'13 - Jul'16	Research Consultant for Consultancy Project "Architectural and Algorithmic Optimizations for Speech based Communication Interfaces on Mobile Devices sponsored by Intel.

Skills:

Programming Languages • C • Python • C++ • CUDA • OpenCL
Libraries & Tools • PyTorch • PocketSphinx • Kaldi • LLVM • Clang